

## SPECIFICATION

### TITLE OF THE INVENTION

Method and Apparatus for Dry-Etching Half-Tone Phase-Shift Films,  
5 Half-Tone Phase-Shift Photomasks and Method for the Preparation  
Thereof, and Semiconductor Circuits and Method for the Fabrication  
Thereof

### BACKGROUND OF THE INVENTION

#### 10 Field of the Invention

The present invention relates to a method for dry-etching a metal thin film or a chromium-containing half-tone phase-shift film and more specifically to a method and an apparatus for dry-etching such a metal thin film, which is applied to a process for preparing a chromium-containing half-tone phase-shift photomask which is used in fabricating, for instance, a semiconductor device as well as a method for dry-etching a chromium-containing half-tone phase-shift film which is used in a pattern-etching process for forming a fine pattern of a metal thin film such as fine electrode patterns (for a flat panel display (FPD) or the like) and color filters. In addition, the present invention also pertains to a chromium-containing half-tone phase-shift photomask which is provided with a pattern formed using such a dry-etching method and a method for preparing the photomask as well as a semiconductor circuit fabricated using such a photomask and a method for fabricating the semiconductor circuit.

#### 25 Description of the Prior Art

As a photomask blank for chromium-containing half-tone phase-shift photomasks, there have been known, for instance, those having such a

structure as shown in Fig. 1, which comprises a glass substrate a formed from, for instance, synthetic quartz glass; a light-shielding film or a half-tone phase-shift film b consisting of a thin film of a metal such as chromium or chromium oxide, formed on the surface of the substrate; and a resist layer c of a light-sensitive/electron-sensitive resin, which is formed on the light-shielding or half-tone phase-shift film. The glass substrate a may serve as a support for patterns and therefore, must have a variety of desired characteristic properties such as high transmittance, high uniformity, defect-free characteristics, resistance to washing and excellent flatness. In addition, the film b may serve as a light-shielding material for patterning or a phase-shift film and therefore, should satisfy the desired requirements for various properties such as etching controllability, uniformity, defect-free characteristics, resistance to washing, low stress and high adhesion to the glass substrate. Moreover, the resist layer c has a role as a film for forming a light-shielding film or a half-tone phase-shift film and accordingly, should have a variety of desired characteristic properties such as high-sensitivity/high resolution, resistance to etching, uniformity, defect-free characteristics and high adhesion to the light-shielding film or the film for forming a half-tone phase-shift film.

A photomask provided thereon with a fine electric circuit pattern has been prepared by wet-etching or dry-etching a chromium light-shielding or half-tone phase-shift film using a photomask blank having such a structure according to the electron beam patterning process or the laser beam patterning process. An example of such a mask-processing scheme is shown in Fig. 2. In this respect, the mask-processing scheme shown in Fig. 2 relates to a light-shielding film of chromium, but the processing scheme for the half-tone phase-shift mask is almost identical to that depicted on Fig. 2 and further details of the processing scheme for forming a half-tone phase-shift

mask are described in Japanese Un-Examined Patent Publication (hereinafter referred to as "J.P. KOKAI") No. Hei 7-140635, the disclosure of which is hereby incorporated by reference.

In the wet-etching, there have recently been highlighted a limit in the dimension control due to the undercut and a limit in the verticality of the etched cross section, and the dry-etching technique has thus been widely used instead.

The dry-etching methods for preparing a photomask and the dry-etching apparatus for practicing the methods are described in, for instance, J.P. KOKAI No. Hei 6-347996, the disclosure of which is hereby incorporated by reference. In this dry-etching technique, a chromium film is etched using a gas comprising, for instance, chlorine gas to which oxygen gas is added, as a reactive ion etching gas.

Moreover, the dry-etching method for preparing a photomask of a chromium-containing film is disclosed in, for instance, Japanese Patent No. 2,765,065, the disclosure of which is hereby incorporated by reference. This patent discloses, in Examples, that when the chromium-containing film is dry-etched by this dry-etching method while using a resist film of a positive electron beam resist EBR-9 (which is available from Toray Industries, Inc.) as a mask and a mixed gas comprising 160 SCCM of chlorine gas, 40 SCCM of oxygen gas and 160 SCCM of wet air as a dry-etching gas, there is not observed any change in the etching rate of the electron beam resist film, while the etching rate of the chromium-containing film increases and the selective (or etching) ratio against the resist film is improved. As a result, the chromium-containing film can sufficiently be patterned by this dry-etching technique. In this connection, the wet air (160 SCCM) in the mixed etching gas comprises about 128 SCCM of nitrogen gas and about 32 SCCM of oxygen gas corresponding to the component ratio of nitrogen to oxygen in

the air which is equal to 4:1.

In addition, the semiconductor circuit has recently become more and more finer and the size of the semiconductor circuit is increasingly reduced from  $0.2 \mu m$  to  $0.15 \mu m$ . For instance, in case of a semiconductor circuit fabricated using a conventional photomask, the dimensional error observed for the memory circuit portion is large as compared with that observed for the peripheral circuit portion in the memory circuit which comprises the memory circuit portion and the peripheral circuit portion, while such an error is also large even in the logic circuit and thus these errors may adversely affect the characteristic properties of the resulting circuit. For this reason, there has been desired for the development of a half-tone phase-shift photomask which permits the fabrication of a circuit whose dimensional difference between circuits within a semiconductor chip is as low as possible.

If a chromium or chromium oxide film as a light-shielding film or a half-tone phase-shift film is subjected to dry-etching using a chlorine-containing gas and if a pattern is formed on a plane at an almost uniform density, the film can be chromium-etched at an approximately uniform rate throughout the whole surface and accordingly, the dimensional control within a plane can be achieved to such an extent that the in-plane uniformity  $3\sigma$  ( $3 \times$  the variance of (measured line width - averaged line width)) ranges from 20 to 60 nm for the line width ranging from 1 to  $2 \mu m$ .

However, dense patterns (patterns whose area occupied by a resist is small) and coarse patterns (patterns whose area occupied by a resist is large) often coexist in the plane of a practical photomask and if the dry-etching technique is used for forming such a photomask, the etching rate of a chromium film is high at the densely patterned portion and low at the coarsely patterned portion. As a result, the dimensional difference within a plane reaches up to about 100 nm for a designed line width ranging from 1

to  $2 \mu$  m. A photomask having such a large dimensional difference within the plane cannot be used for the fabrication of, for instance, circuits having a higher integration density such as memory circuits, logic circuits and LSI circuits.

5        The Japanese Patent No. 2,765,065 described above does not relate to the solution of the foregoing problems, but relates to the improvement of the selective ratio of a chromium film to a resist film.

## SUMMARY OF THE INVENTION

10      Accordingly, an object of the present invention is generally to solve the foregoing problems associated with the conventional technique or an improved dry-etching technique as a means for forming a fine pattern, which permits the reduction of the dimensional difference due to the coexistence of coarse and dense patterns within a plane, for instance, a dry-etching  
15      technique for manufacturing a chromium-containing half-tone phase-shift photomask. More specifically, it is an object of the present invention to provide a dry-etching method and a dry-etching apparatus, which permit the production of a high precision photomask by reducing the dimensional difference due to the coexistence of coarse and dense patterns within a  
20      plane.

Another object of the present invention is to provide a method for preparing a photomask using the foregoing dry-etching method and to provide a photomask thus prepared.

25      A further object of the present invention is to provide a method for fabricating a semiconductor device using the photomask and a semiconductor circuit fabricated by the method.

The inventors of the present invention have conducted various studies to achieve the foregoing objects, have found that even in the production of

a chromium-containing half-tone phase-shift photomask in which dense patterns and coarse patterns coexist in the plane thereof, the use of a mixed etching gas comprising an oxygen-containing halogen gas such as an oxygen-containing chlorine gas (e.g.,  $\text{Cl}_2 + \text{O}_2$ ), to which at least a hydrogen-containing gas (e.g.,  $\text{H}_2$ , hydrogen chloride ( $\text{HCl}$ ) gas) is added, in the etching of the chromium-containing half-tone phase-shift film permits the achievement of in-plane dimensional control almost identical to that achieved for a mask in which patterns are formed in the plane at an almost uniform density, i.e., such in-plane dimensional control that the dimensional difference is not more than a half of that conventionally attained, for instance, 10 to 20 nm (0.010 to 0.020  $\mu\text{m}$ ) and thus have completed the present invention on the basis of such a finding for the designed line width ranging from 1 to 2  $\mu\text{m}$ .

According to a first aspect of the present invention, there is provided a dry-etching method characterized by using, as an etching gas, a mixed gas including (a) a reactive ion etching gas, which contains an oxygen-containing gas and a halogen-containing gas, and (b) a reducing gas added thereto, in a process for dry-etching a metal thin film as a chromium-containing half-tone phase-shift film.

According to a second aspect of the present invention, there is provided a method for preparing a chromium-containing half-tone phase-shift photomask by performing a series of pattern-forming steps such as a step for forming a resist layer on a photomask blank, a step for exposing and patterning the resist layer, a developing step, a step for etching the photomask blank and a step for removing the resist layer and which is characterized in that patterns to be transferred onto a wafer are formed on the photomask blank for the chromium-containing half-tone phase-shift photomask according to the dry-etching method described above to thus give

a photomask.

According to a third aspect of the present invention, there is provided a chromium-containing half-tone phase-shift photomask which is prepared through a series of pattern-forming steps such as a step for forming a resist layer on a photomask blank, a step for exposing and patterning the resist layer, a developing step, a step for etching the photomask blank and a step for removing the resist layer and which is characterized in that patterns to be transferred onto a wafer are formed on the photomask blank for the chromium-containing half-tone phase-shift photomask according to the dry-etching method described above to thus give a photomask.

According to a fourth aspect of the present invention, there is provided a method for manufacturing a semiconductor circuit which comprises the steps of transferring the patterns formed on the chromium-containing half-tone phase-shift photomask according to the third aspect of the invention on a wafer on which a light-sensitive material is coated, developing the light-sensitive material to form resist patterns on the wafer, or to manufacture a semiconductor circuit which comprises coexisting coarse and dense patterns corresponding to the resist patterns.

According to a fifth aspect of the present invention, there is provided a semiconductor circuit which has a circuit comprising coexisting coarse and dense patterns corresponding to the resist patterns formed by transferring the resist patterns formed on the chromium-containing half-tone phase-shift photomask according to the third aspect of the invention on a wafer on which a light-sensitive material is coated and then developing the light-sensitive material.

According to a sixth aspect of the present invention, there is provided a dry-etching apparatus used in dry-etching a metal thin film as a chromium-containing half-tone phase-shift film, which is provided with a sequencer for

establishing dry-etching conditions, wherein the metal thin film is a chromium-containing half-tone phase-shift film consisting of a chromium film, a chromium oxide film, a chromium nitride film, chromium oxynitride film, chromium fluoride film or a laminated film thereof; wherein if an etching gas used consists of chlorine, oxygen and hydrogen gases, the relative flow rates of these gases as expressed in terms of % by volume range from 66 to 46, 17 to 11 and 18 to 41 % by volume, respectively, or if an etching gas used consists of chlorine, oxygen and hydrogen chloride gases, the relative flow rates of these gases as expressed in terms of % by volume range from 58 to 44, 15 to 11 and 28 to 45 % by volume, respectively; and wherein the apparatus is designed in such a manner that when inputting the parameters relating to the foregoing dry-etching conditions, directly or through a memory device of a computer, to the sequencer and then starting the dry-etching process, the dry-etching is automatically carried out under the foregoing dry-etching conditions.

According to a seventh aspect of the present invention, there is provided a dry-etching apparatus which comprises an etching chamber, a transport chamber, a substrate cassette bed and a sequencer for establishing dry-etching conditions, wherein four electromagnets each comprising a square-shaped ring-like coil are provided on the outer side of the etching chamber, two each of these electromagnets being opposite to one another and making a pair, these electromagnets being so designed that when applying a low frequency current which is 90 deg. out of phase thereto, the combined magnetic field established by these two paired electromagnets can rotate in a plane parallel to a substrate at a frequency identical to that of the low frequency current, an RF electrode and an opposite electrode are disposed in the etching chamber, a transport robot for transporting the substrate is provided in the transport chamber, the transport robot being a two-joint robot

having two knots, the tip of a transport arm thereof being able to undergo advancing, reciprocating and rotating motions due to the composition of rotational motions of a motor axis and these two knots within each horizontal plane, the robot thus transporting the substrate, wherein a metal thin film to be dry-etched is a chromium-containing half-tone phase-shift film consisting of a chromium film, a chromium oxide film, a chromium nitride film, chromium oxynitride film, chromium fluoride film or a laminated film thereof, wherein if an etching gas used consists of chlorine, oxygen and hydrogen gases, the relative flow rates of these gases as expressed in terms of % by volume range from 66 to 46, 17 to 11 and 18 to 41 % by volume, respectively, or if an etching gas used consists of chlorine, oxygen and hydrogen chloride gases, the relative flow rates of these gases as expressed in terms of % by volume range from 58 to 44, 15 to 11 and 28 to 45 % by volume, respectively, and wherein the apparatus is designed in such a manner that when inputting the parameters relating to the foregoing dry-etching conditions, directly or through a memory device of a computer, to the sequencer and then starting the dry-etching process, the dry-etching is automatically carried out under the foregoing dry-etching conditions.

## 20 Brief Description of the Drawings

The aforementioned and other objects, features and advantages of the present invention will become more apparent from the following description taken with reference to the accompanying drawings, wherein

25 Fig. 1 is a cross sectional view showing the structure of a photomask blank;

Fig. 2 is a mask process flow diagram for explaining the process for preparing a photomask;

Fig. 3 is a partially cutaway plan view showing a dry-etching apparatus

used for carrying out the present invention;

Fig. 4 is a cross sectional view of the dry-etching apparatus shown in Fig. 3 taken along the line A-A;

Fig. 5(A) is a schematic plan view showing the arrangement of test patterns used in Example 1 and Fig. 5(B) shows the pattern arrangement as shown in Fig. 5(A) and is a schematic plan view showing the positions (or lines A-A' and B-B') along which the cross sectional views shown in Figs. 6(A) and 6(B) are taken;

Fig. 6(A) is a flow diagram for explaining the preparation of measuring patterns (or test patterns) at a densely patterned portion along the line (A-A') in Fig. 5(B), and Fig. 6(B) is a flow diagram for explaining the preparation of measuring patterns at a coarsely patterned portion along the line (B-B') in Fig. 5(B);

Fig. 7 is a graph showing the influence of the addition of hydrogen gas or hydrogen chloride gas to the etching gas, on the change of the dimensional difference due to the coexistence of coarse and dense patterns observed when the test pattern shown in Fig. 5 is formed by etching;

Fig. 8 is a schematic diagram showing the structure of a memory circuit of the present invention, which comprises a memory circuit portion and a peripheral circuit portion; and

Fig. 9 is a schematic diagram showing the structure of a system LSI circuit of the present invention which comprises combined memory circuit and logic circuit portions.

## 25 Description of the Preferred Embodiments

The dry-etching method according to the present invention is carried out using, as a reactive etching gas, a mixed gas comprising (a) a reactive etching gas, which consists of an oxygen-containing gas and a halogen-

containing gas, and (b) a reducing gas added thereto in the process for dry-etching a metal thin film. This metal thin film may be a thin film such as a chromium film, a chromium oxide film, a chromium nitride film, a chromium oxynitride film, a chromium fluoride film or a laminated film thereof.

5 In addition, the reducing gas used herein may be a gas containing at least hydrogen, i.e., hydrogen gas; a hydrocarbon gas selected from the group consisting of  $C_nH_{2n+2}$  ( $n=1$  to 8),  $C_nH_{2n}$  ( $n=2$  to 10),  $C_nH_{2n-2}$  ( $n=2$  to 8); an alcoholic gas selected from the group consisting of  $CH_3OH$ ,  $C_2H_5OH$ ,  $CH_3CH_2CH_2OH$ ,  $(CH_3)_2CHOH$ ,  $(CH_3)_3COH$ ,  $CH_2=CHCH_2OH$ ; a hydrogen halide gas selected from the group consisting of HF, HCl, HBr and HI; 10 ammonia gas; or water.

If the metal thin film or the chromium-containing half-tone phase-shift film is a chromium film, a chromium oxide film, a chromium nitride film, 15 chromium oxynitride film, chromium fluoride film or a laminated film thereof and if the etching gas or the mixed gas used consists of chlorine, oxygen and hydrogen gases, the flow rates of these gases as expressed in terms of % by volume preferably range from 66 to 46, 17 to 11 and 18 to 41 % by volume, respectively, while if the metal thin film is the foregoing 20 chromium-containing half-tone phase-shift film and if the mixed gas used consists of chlorine, oxygen and hydrogen chloride gases, the flow rates of these gases as expressed in terms of % by volume preferably range from 58 to 44, 15 to 11 and 28 to 45 % by volume, respectively. This is because if the flow rates each is beyond the foregoing range, it is difficult to obtain a highly precision half-tone phase-shift mask having a reduced dimensional difference 25 due to the coexistence of coarse and dense patterns in a plane.

Moreover, the oxygen-containing gas used in the invention may be a gaseous oxygen-containing compound which can be an oxygen source, such as  $O_2$ , CO,  $CO_2$ , NO and  $N_2O$  and the halogen-containing gas usable herein

may be a gaseous halogen (such as chlorine, fluorine)-containing compound such as chlorine gas,  $\text{CCl}_4$ ,  $\text{CF}_2\text{Cl}_2$ ,  $\text{CFCl}_3$  or  $\text{CF}_3\text{Cl}$ , which is a commonly used reactive ion etching gas.

The dry-etching apparatus used for practicing the dry-etching method according to the present invention is not restricted to any particular one and may be, for instance, an apparatus which makes use of a variety of systems such as barrel type, RIE, MERIE, ICP, NLD and ECR. Preferred are those depicted in Figs. 3 and 4, which are equipped with a sequencer for establishing dry-etching conditions, wherein the metal thin film is a chromium-containing half-tone phase-shift film consisting of a chromium film, a chromium oxide film, a chromium nitride film, chromium oxynitride film, chromium fluoride film or a laminated film thereof; wherein if the mixed gas used as etching gas consists of chlorine, oxygen and hydrogen gases, the flow rates of these gases as expressed in terms of % by volume range from 66 to 46, 17 to 11 and 18 to 41 % by volume, respectively, or if the etching gas used consists of chlorine, oxygen and hydrogen chloride gases, the flow rates of these gases as expressed in terms of % by volume range from 58 to 44, 15 to 11 and 28 to 45 % by volume, respectively; and wherein the apparatus is designed in such a manner that when inputting the parameters relating to the foregoing dry-etching conditions, directly or through a memory device of a computer, to the sequencer and then starting the dry-etching process, the dry-etching is automatically carried out under the foregoing dry-etching conditions.

The dry-etching apparatus equipped with the foregoing sequencer according to the present invention comprises an etching chamber, a transport chamber and a substrate cassette bed, wherein four electromagnets each comprising a square-shaped ring-like coil are provided on the outer side of the etching chamber, two each of these electromagnets being opposite to one

another and making a pair, these electromagnets being so designed that when applying a low frequency current which is 90 deg. out of phase thereto, the combined magnetic field established by these two paired electromagnets can rotate in a plane parallel to the substrate at a frequency identical to that of the low frequency current, an RF electrode and an opposite electrode being disposed in the etching chamber, a transport robot for transporting the substrate being provided in the transport chamber, the transport robot being a two-joint robot having two knots, the tip of the transport arm thereof being able to undergo advancing, reciprocating and rotating motions due to the composition of rotational motions of a motor axis and these two knots within each horizontal plane, and the robot thus transporting the substrate.

In Examples given later, all of the pressure, RF electric power, magnetic field, distance between electrodes, kinds of etching gases and the flow rate ratio: Cl<sub>2</sub>/O<sub>2</sub> in the etching gas mixture are fixed to predetermined values respectively, but they are not restricted to these specific values and the dry-etching operations may be performed under the following conditions, if an MERIE apparatus is, for instance, used:

Pressure: 1.3 to 66.7 Pa (10 to 500 mTorr)

RF Electric Power: 10 to 300 W

(RF Electric Power density: 0.10 to 0.4 W/cm<sup>2</sup>)

Flow Rate Ratio, O<sub>2</sub>/(Cl<sub>2</sub> + O<sub>2</sub>): 10 to 25 %

Cl<sub>2</sub>/O<sub>2</sub>: 20 to 160/5 to 100 SCCM

Magnetic Field: 0 to 150 Gs

Interelectrode Distance: 40 to 120 mm

According to the present invention, a chromium-containing half-tone phase-shift mask can be prepared by a series of well-known pattern-forming steps such as a step for forming a resist layer on a photomask blank, a step

for exposing and patterning the resist layer, a developing step, a step for etching the photomask blank and a step for removing the resist layer, wherein patterns to be transferred onto a wafer are formed on the photomask blank using the dry-etching method described above as the dry-etching process.

5 In addition, the chromium-containing half-tone phase-shift mask of the present invention is characterized in that patterns to be transferred onto a wafer are formed on a photomask blank using the dry-etching method described above as the dry-etching process among a series of the foregoing well-known pattern-forming steps.

10 The present invention further permits the manufacture of a semiconductor circuit by transferring the resist patterns formed on the chromium-containing half-tone phase-shift photomask produced by the foregoing method on a wafer on which a light-sensitive material is coated, developing the light-sensitive material to form the resist patterns on the  
15 wafer, and then subjecting the wafer to etching such as dry-etching or ion-implantation on the basis of the resist patterns thus formed on the wafer to thereby form a circuit having patterns corresponding to the resist patterns. Examples of semiconductor circuits thus obtained include a memory circuit in which patterns are regularly arranged, a logic circuit comprising randomly  
20 arranged patterns, and a system LSI circuit comprising combined memory and logic circuits.

The following are characteristic properties of the semiconductor circuit obtained using the chromium-containing half-tone phase-shift mask of the present invention. For instance, a memory circuit comprises a memory circuit portion on which patterns are regularly arranged and a peripheral circuit portion on which patterns are irregularly arranged to ensure the connection to the exterior and therefore, the areas occupied by the patterns in these circuit portions are different from one another. More specifically, in  
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a gate-forming process in manufacturing a transistor which has an important influence upon the characteristics of the resulting circuit, the rate of area to be removed for patterning in the peripheral circuit portion is high as compared with that observed for the memory cell portion. The 5 semiconductor circuit has recently become more and more finer and the size of the semiconductor circuit is increasingly reduced from  $0.2 \mu m$  to  $0.15 \mu m$ . In case of the semiconductor circuit fabricated using the chromium-containing half-tone phase-shift mask according to the present invention, the dimensional difference observed between the memory cell and peripheral 10 circuit portions is very small, the variation in the dimension is also small and therefore, the characteristics of the circuit are not adversely affected at all. For this reason, the present invention permits the manufacture of an excellent semiconductor circuit whose memory cell and peripheral circuit portions have almost the same characteristic properties.

15 The same effect is also observed for the logic circuit in which patterns are randomly arranged and the distribution of the area to be removed for patterning is also random and thus the invention permits the manufacture of a quite excellent semiconductor circuit having a very low dimensional difference within the chip. The present invention can further be applied to 20 the production of a system LSI circuit comprising a combination of a memory circuit portion and a logic circuit portion. In this case, the memory circuit portion and the logic circuit portion differ from each other in the packing densities of devices and the densities of wiring, but the dimensional difference between the memory and logic circuit portions is very small and therefore, does not adversely affect the characteristic properties of the 25 resulting circuit. As a result, a good semiconductor circuit can thus be produced, which does not have any difference between the memory and logic circuit portions in their characteristics.

The present invention will hereinafter be described in more detail with reference to the following Examples and attached figures, but these Examples are given only for the purpose of illustration and the present invention is not restricted to these specific Examples at all.

5       The dry-etching apparatus (MERIE apparatus) used in the following Examples is shown in Figs. 3 and 4. This dry-etching apparatus 1 is so designed that an etching chamber 2, a transport chamber 3 and a substrate cassette bed 4 are accommodated in a panel 12 which constitutes the outer periphery of the dry-etching apparatus and that the etching chamber 2 is  
10      accommodated in electromagnets 5, 6, 7, 8 disposed on the outer periphery of the chamber 2. Each electromagnet comprises a square-shaped ring-like coil, the electromagnets 5 and 6 and the electromagnets 7 and 8 make pairs respectively and a low frequency current whose phase is shifted, for instance, 90 deg. is passed through these electromagnets. These electromagnets are so  
15      designed that the coils of the paired electromagnets are wound in the same direction and the combined magnetic field established by these two paired electromagnets 5 and 6, and 7 and 8 can rotate in a plane parallel to the substrate at a frequency identical to that of the low frequency current as shown by the dotted arrows in Figs. 3 and 4. Disposed within the etching  
20      chamber 2 are a plate-like RF electrode 10 which is connected to an RF power supply 9 through a condenser 13 and a plate-like opposite electrode 14 and a substrate 11 may be placed on the RF electrode 10 through a substrate delivery port 15 formed on the side of the etching chamber 2.

25       The opposite electrode 14 and the etching chamber 2 are maintained at the ground voltage. To supply a reactive gas for etching to the etching chamber 2, a gas supply system 16 is disposed at a reactive gas supply port 30, which is provided with a gas bomb and a mass flow controller and an exhaust system 17 is connected to a vacuum exhaust port 18 of the etching

chamber 2, which is equipped with a vacuum pump for controlling the gas pressure in the etching chamber 2. The reactive gas herein used is one comprising an oxygen-containing mixed gas and a reducing gas at least containing hydrogen, as has already been discussed above.

5        A plurality of substrates 11 are accommodated in a cassette case 19, then the cassette case is put on the substrate cassette bed 4, each substrate delivered from the cassette case 19 is brought into the transport chamber 3 through a partition valve 20 by the action of a transport robot 21 and then placed on the RF electrode 10 in the etching chamber 2 through a vacuum valve 22 and the substrate delivery port 15. The transport robot 21 is a known two-joint robot having two knots 26 and 28 and is so designed that the tip of a transport arm 29 thereof can undergo reciprocating and rotating motions due to the composition of rotational motions of a motor axis 24 and these two knots 26 and 28. The motions of a first arm 25 and a second arm  
10      27 are restricted to only those within the horizontal planes. The movement of the tip of the transport arm 29 between the cassette case 19 and the transport chamber 3 through the partition valve 20 and that of the tip of the transport arm 29 between the RF electrode 10 in the etching chamber 2 and the transport chamber 3 through the substrate delivery port 15 are advancing  
15      and reciprocating motions due to the composition of rotational motions of each arm 25, 27, 29 of the robot at the motor axis 24 and the foregoing two knots 26 and 28. The transport of the substrate between the vacuum valve 22 at the upstream side of the substrate delivery port 15 and the partition valve 20 on the side of the cassette case 19 is performed by the half turn motion,  
20      within a horizontal plane, of the transport arm 29 of the robot, wherein the motor axis 24 serves as a rotating center. If a motor 23 disposed on the exterior of the transport chamber 3 is rotated, the plate-like transport arm 29 carrying a substrate 11 undergoes reciprocating and rotational motions to  
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thus transport the substrate between the cassette case 19 and the RF electrode 10.

When etching a pattern-forming material of the substrate 11 on the RF electrode 10, the etching chamber 2 is evacuated by operating the exhaust system 17, followed by introduction of a reactive gas into the chamber 2 through the reactive gas supply port 30, excitation of the two pairs of electromagnets 5, 6, 7, 8 and application of an RF electric power to the RF electrode 10 to thus generate plasma. If the same low frequency alternating current is passed through these two paired electromagnets 5 and 6, and 7 and 8 in the same direction, while shifting the phase of the current applied to either of the paired electromagnets 90 deg. relative to that of the other current, a rotating magnetic field is established in a plane parallel to the substrate 11. The plasma generated between the RF electrode 10 and the opposite electrode 14 is concentrated on the surface of the substrate 11 by the action of the rotating magnetic field and this leads to an increase of its density. Thus, the reactive gas introduced into the chamber is highly efficiently dissociated and the substrate 11 is subjected to reactive ion-etching under such a condition that only a slight DC bias voltage is generated on the substrate.

For instance, it is assumed that the dry-etching is carried out using, as the substrate 11, a photomask substrate which comprises a transparent substrate of synthetic quartz or the like, a thin layer of a pattern-forming material such as Cr, Cr provided with an antireflection film or SiO<sub>2</sub> which is applied onto the transparent substrate and a patterned photoresist layer (for instance, a resist for EB exposure (e.g. ZEP-810S (trade name) available from Nippon Zeon Co., Ltd.)) provided on the pattern-forming material. In this case, if the substrate 11 is dry-etched with the introduced reactive gas under the same conditions used for the dry-etching of the IC substrate, this

results in insufficient selection ratio of the material to be etched to the resist and insufficient in-plane dimensional uniformity of the photomask since the photomask substrate 11 is not made of silicon, but is made from synthetic quartz unlike the IC substrate and the pattern-forming material applied thereon does not comprise poly Si or an oxide layer + poly Si, but comprises Cr, Cr provided with an antireflection film or  $\text{SiO}_2$ , unlike the IC substrate. However, the selection ratio and the in-plane dimensional uniformity can be improved and a highly precision photomask can be produced if establishing the following dry-etching conditions: a magnetic field intensity ranging from 5 50 to 150 Gs; a pressure of the reactive gas in the etching chamber 2 ranging from 0.03 to 0.3 Torr (4 to 40 Pa); and an RF electric power density on the RF electrode 10 ranging from 0.20 to 0.32 W/cm<sup>2</sup>.

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The following are specific Examples of the present invention.

15      **Example 1**

A dry-etching method carried out according to the present invention will be described in this Example. Test patterns used in this Example are shown in Figs. 5(A) and 5(B) and the flow diagrams for illustrating the preparation of test samples are shown in Figs. 6(A) and 6(B). Fig. 6(A) is a flow diagram, as expressed in terms of schematically cross sectional diagrams, for explaining the preparation of measuring patterns at a densely patterned portion along the line (A-A') in Fig. 5(B), and Fig. 6(B) is a flow diagram, as expressed in terms of schematically cross sectional diagrams, for explaining the preparation of measuring patterns at a coarsely patterned portion along the line (B-B') in Fig. 5(B). As will be clear from the flow diagrams shown in Figs. 6(A) and (B), each test sample was prepared through processes for (a) EB patterning, (b) developing, (c) etching and (d) removing the resist. In Figs. 6(A) and (B), d represents a substrate, e represents, in this

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case, a chromium oxide film and f represents a resist layer. The details of the etching processes shown in Figs. 6(A) and (B) are as follows:

The conditions for operating the MERIE apparatus as shown in Figs. 3 and 4, which permit the dry-etching of a chromium film or a chromium oxynitride film using a conventional chlorine-containing gas have already been described above, but in this Example, the test patterns as shown in Figs. 5(A) and (B) were formed by dry-etching a photomask blank for chromium oxide half-tone phase-shift masks under the conditions disclosed in Table 1, using an etching gas comprising the foregoing gas system ( $\text{Cl}_2/\text{O}_2 = 80/20$  SCCM) to which hydrogen gas was added in an amount specified in the following Table 1 and using the MERIE apparatus as shown in Figs. 3 and 4, on the basis of the following working conditions of the apparatus among others:

Pressure:	6.8 Pa (50 mTorr)
RF Power Supply:	80 W
Gas:	$\text{Cl}_2/\text{O}_2 = 80/20$ SCCM
Magnetic Field:	50 to 60 Gs
Interelectrode Distance:	60 mm

The photomask blank for chromium oxide half-tone phase-shift mask used in this Example was one prepared by applying a single chromium oxide layer in a thickness of about 900 Å as a pattern-forming material onto the surface of a square-shaped synthetic quartz substrate having a size of 152.4×152.4 mm and a thickness of 6.35 mm and then applying a resist layer (ZEP-810S) for EB exposure onto the layer of the pattern-forming material. Moreover, the test patterns as shown in Figs. 5(A) and (B) comprised, after the EB exposure and the development, a dimension-evaluation pattern (about 6.5 mm × 35 mm) which was arranged at the central portion of the left half

(dense portion) of the mask and which included a plurality of L/S (Line and Space), isolated L and isolated S patterns therein; a completely removed pattern (the pattern of the exposed chromium oxide having a size of 46 mm × 54 mm) surrounding these patterns; and a dimension-evaluation pattern which was arranged at the central portion of the right half (coarse portion) of the mask. After the EB exposure and the development, the dry-etching was carried out using hydrogen gas as the added gas. The pattern-forming conditions are summarized in Table 1.

Shown in Fig. 7 are the results thus observed when adding 0 to 72 SCCM of hydrogen gas to the etching gas and forming test patterns (Figs. 5(A) and (B)) which comprised dense and coarse patterns arranged in a plane, or the variation of the dimensional difference between the coarse and dense portions (i.e., the difference in the dimension between the coarse and dense portions) as a function of the added amount of the hydrogen gas. In addition, shown in the following Table 2 are the changes in the dimension of the coarse and dense portions as a function of the amount of the hydrogen gas which is added to the etching gas when the test patterns are formed through dry-etching. Moreover, shown in the following Table 3 are the dimensional variation in the coarse and dense portions of the resist pattern obtained after the development as a function of the added amount of the hydrogen gas.

The following were found, as a result of the dry-etching performed under the foregoing conditions:

As will be seen from the results shown in Table 2 and Fig. 7, there is observed a dimensional difference of about  $0.053 \mu\text{m}$  between the coarse and dense portions when any hydrogen gas was not added to the etching gas, while the dimensional difference therebetween is reduced as the added amount of the hydrogen gas increases. More specifically, when an increasing amounts of hydrogen gas ranging from 21.6 to 72 SCCM were added to, in

order, Sample Nos. 1-2, 1-3, 1-4 and 1-5, the dimensional difference between the coarse and dense portions of the pattern was found to be about 0.011 to 0.002  $\mu$  m which corresponded to not more than about 1/5 to 1/26 times that (0.053  $\mu$  m) observed when any hydrogen gas was not added. This clearly  
5 indicates that the problem of the dimensional difference between the coarse and dense portions is considerably eliminated. As shown in Table 1, the gas flow rates of Cl<sub>2</sub>, O<sub>2</sub> and H<sub>2</sub> and the relative flow rates (as expressed in terms of % by volume) used in the dry-etching of the samples 1-2, 1-3, 1-4 and 1-5 are 80, 20 and 21.6 to 72 SCCM and 65.79 to 46.51, 16.45 to 11.63  
10 and 17.76 to 41.86 % by volume, respectively.

As has been discussed above, it has been proved that the conditions for dry-etching the samples 1-2, 1-3, 1-4 and 1-5 are optimum ones and accordingly, the apparatus was so designed that the parameters relating to the foregoing dry-etching conditions listed in Table 1 were inputted, directly or  
15 through a memory device of a computer, to the sequencer and then the dry-etching process was started to automatically carry out the dry-etching under the foregoing optimum dry-etching conditions.

## Example 2

20 A dry-etching method carried out according to the present invention will be described in this Example. The photomask blank for preparing a chromium oxide half-tone phase-shift mask and the test patterns used in this Example were the same as those used in Example 1. The scheme for preparing the test samples were the same as that used in Example 1 except that hydrogen chloride (HCl) gas, as the gas to be added to the etching gas,  
25 was substituted for the hydrogen gas used in Example 1 in the etching step, that ZEP7000 (the trade name of a product available from Nippon Zeon Co., Ltd.) was substituted for the ZEP-810S as the resist for the EB exposure and

that the resist film formed had a thickness of 4000 Å. The pattern-forming conditions are listed in the following Table 1 together with those used in Example 1.

HCl gas was added to the etching gas in an amount ranging from 0 to 5 140 SCCM and test patterns (Figs. 5(A) and (B)) having coarse and dense portions in a plane were formed by etching. Regarding the results observed after carrying out the etching under the conditions specified in Table 1, the change in the averaged dimensional difference between the coarse and dense portions (averaged value of the difference between the dimensions of the 10 coarse and dense portions) is shown in Fig. 7 as a function of the added amount of the HCl gas while comparing these results with those obtained in Example 1 in which hydrogen gas was used as the added gas component. In addition, listed in Table 2 are the dimensional variation in the coarse and dense portions as a function of the added amount of HCl gas observed when 15 the test patterns were formed through dry-etching, together with those observed in Example 1 wherein hydrogen gas was used as the added gas. In addition, the dimensions of the resist patterns after the development are the same as those listed in Table 3 which are observed when using hydrogen gas as the added gas.

As will be seen from the data shown in Table 2 and Fig. 7, there is 20 observed a dimensional difference of about  $0.052 \mu\text{m}$  between the coarse and dense portions when any HCl gas was not added to the etching gas, while the dimensional difference therebetween was reduced as the added amount of the HCl gas increased. More specifically, the results of Sample Nos. 2-2, 2-3 and 25 2-4 indicate that when HCl gas was added in an amount ranging from 40 to 80 SCCM, the dimensional difference between the coarse and dense portions of the patterns was in the range of from about 0.010 to  $0.020 \mu\text{m}$  which corresponded to about 1/5.2 to 1/2.6 times that observed when any HCl gas

was not added. This clearly indicates that the problem of the dimensional difference between the coarse and dense portions is considerably eliminated. The result of dimensional difference may further be improved by optimizing the relative flow rates of gas components in the etching gas. As shown in 5 Table 1, the gas flow rates of Cl<sub>2</sub>, O<sub>2</sub> and HCl and the relative flow rates (expressed in terms of % by volume) used in the dry-etching of the samples 2-2, 2-3 and 2-4 are 80, 20 and 40 to 80 SCCM; and 57.14 to 44.44, 14.28 to 11.11 and 28.57 to 44.44 % by volume, respectively.

Moreover, the etching operation may be safer by the use of HCl gas in 10 place of hydrogen gas as the added gas.

As has been described above in detail, it has been proved that the conditions for dry-etching the samples 2-2, 2-3 and 2-4 are optimum ones and accordingly, the apparatus was so designed that the parameters relating to the foregoing dry-etching conditions listed in Table 1 were inputted, 15 directly or through a memory device of a computer, to the sequencer and then the dry- etching process was started to automatically carry out the dry-etching under the foregoing optimum dry-etching conditions.

Table 1 : Pattern-Forming Conditions (Cr-Containing Half-Tone)

Ex. No.	Added Etching Gas	Sample No.	Resist Layer	Dry-Etching Conditions									
				Resist	Thickness of Resist	Cl <sub>2</sub>	O <sub>2</sub>	Added Gas	RF Power Supply	Pressure	Magnetic Field		
			Å	SCCM	SCCM	W	Pa	Gauss	mm	sec	sec		
1	H <sub>2</sub>	1-1	ZEP810S	5000	(80) 80 65.79 80 60.38 80 55.83 80 46.51 80	(20) 20 (16.45) 20 (15.09) 20 (13.96) 20 (11.63) 20	(0) 0 (17.76) 21.6 (24.53) 32.5 (30.22) 43.3 (41.86) 72	80	6.8	50-60	60	161	483
		1-2	"	"	"	"	"	"	"	"	"	106	318
		1-3	"	"	"	"	"	"	"	"	"	120	360
		1-4	"	"	"	"	"	"	"	"	"	112	336
		1-5	"	"	"	"	"	"	"	"	"	127	381
		2-1	ZEP7000	4000	(80) 80 (57.14) 80 (50.00) 80 (44.44) 80 (33.33) 80	(20) 20 (14.28) 20 (12.50) 20 (11.11) 20 (8.33) 20	(0) 0 (28.57) 40 (37.50) 60 (44.44) 80 (58.33) 140	80	6.8	50-60	60	161	483
2	HCl	2-2	"	"	"	"	"	"	"	"	"	85	255
		2-3	"	"	"	"	"	"	"	"	"	96	288
		2-4	"	"	"	"	"	"	"	"	"	90	270
		2-5	"	"	"	"	"	"	"	"	"	102	306

(Note) The numerical value in the parenthesis corresponds to the flow rate expressed in terms of "by volume".

Table 2 : Dimensions of Patterns Observed After Dry-Etching (Cr-Containing Half-Tone)

Ex. No.	Added Etching Gas	Sample No.	Flow Rate of Added Gas SCCM	Dense Portion (Large Removed Area) [ $\mu$ m]				Coarse Portion (Small Removed Area) [ $\mu$ m]				Difference Between Coarse and Dense Portions (Coarse-Dense) [ $\mu$ m]				
				L/S Line	L/S Space	ISO Line	ISO Space	L/S Line	L/S Space	ISO Line	ISO Space	L/S Line	L/S Space	ISO Line	ISO Space	Average (Absolute Value)
1	$H_2$	1-1	0	1.999	2.007	1.993	2.012	2.055	1.960	2.052	1.962	2.056	-0.047	0.059	-0.050	0.053
		1-2	21.6	1.997	2.010	1.987	2.010	1.986	2.019	1.980	2.026	-0.011	0.009	-0.007	0.016	0.011
		1-3	32.5	1.967	2.041	1.949	2.047	1.963	2.044	1.950	2.047	-0.004	0.003	0.001	0.000	0.002
		1-4	43.3	1.462	2.039	1.956	2.038	1.967	2.034	1.960	2.044	0.005	-0.005	0.004	0.006	0.005
		1-5	72	1.941	2.059	1.931	2.066	1.950	2.052	1.949	2.056	0.009	-0.007	0.018	-0.010	0.011
2	HCl	2-1	0	1.966	2.036	1.949	2.047	2.017	1.993	2.016	2.000	0.051	-0.043	0.067	-0.047	0.052
		2-2	40	2.017	1.986	2.010	1.975	2.010	2.002	2.005	2.011	-0.007	0.016	-0.005	0.036	0.016
		2-3	60	1.893	2.110	1.874	2.101	1.908	2.098	1.887	2.102	0.015	-0.012	0.013	0.001	0.010
		2-4	80	1.895	2.105	1.878	2.109	1.916	2.079	1.900	2.098	0.021	-0.026	0.022	-0.011	0.020
		2-5	140	1.864	2.136	1.855	2.142	1.912	2.085	1.910	2.087	0.048	-0.051	0.055	-0.055	0.052

**Table 3 : Dimensions of Resist Patterns Observed After Development (Cr-Containing Half-Tone)**

Ex. No.	Added Etch-ing Gas	Sample No.	Flow Rate of Added Gas	Dense Portion (Large Removed Area) [ $\mu\text{m}$ ]				Coarse Portion (Small Removed Area) [ $\mu\text{m}$ ]				Difference Between Coarse and Dense Portions (Coarse-Dense) [ $\mu\text{m}$ ]			
				SCCM	L/S Line	L/S Space	ISO Line	ISO Space	L/S Line	L/S Space	ISO Line	ISO Space	L/S Line	L/S Space	ISO Line
1	$\text{H}_2$	1-1	0	2.119	1.365	2.118	1.355	2.129	1.350	2.126	1.345	0.010	-0.015	0.008	-0.010
		1-2	21.6	2.135	1.334	2.134	1.344	2.136	1.343	2.135	1.350	0.001	0.009	0.001	0.006
		1-3	32.5	2.117	1.369	2.114	1.370	2.117	1.361	2.116	1.367	0.000	-0.008	0.002	-0.003
		1-4	43.3	2.101	1.362	2.106	1.363	2.106	1.360	2.108	1.356	0.005	-0.002	0.002	-0.007
		1-5	72	2.096	1.388	2.089	1.373	2.103	1.383	2.093	1.374	0.007	-0.005	0.004	0.001

(Note) L/S : Line and Space      L: Line      S: Space      ISO: Isolated

### Example 3

In the light of the results obtained in Examples 1 and 2, it was found that the use of hydrogen and hydrogen chloride gases in dry-etching as the gas component to be added to the etching gas permits the reduction of the dimensional difference due to the coexistence of coarsely and densely patterned portions in a plane and the formation of high precision patterned products by etching. Accordingly, there were prepared several kinds of photomasks according to the procedures used, in Examples 1 and 2, for preparing the test samples carrying test patterns, or by forming a resist layer on a photomask blank for a chromium or chromium oxide half-tone phase-shift photomask, followed by a series of well-known pattern-forming steps such as light-exposure, development, etching and washing. The resulting photomasks each had patterns such as hole systems or line-and-space patterns to be transferred to a wafer and comprised, on a plane, coexisting coarse and dense patterns. The resulting chromium oxide half-tone phase-shift photomask was found to have a very small dimensional difference between the coarse and dense portions of the patterns. These results clearly indicate that the use of the foregoing added gas permits the achievement of quite satisfactory results, i.e., substantial reduction of the dimensional difference, like the results observed in Examples 1 and 2.

### Example 4

A semiconductor circuit was formed on a wafer by repeating the following steps (1) to (4) using the chromium oxide half-tone phase-shift photomask prepared in Example 3:

- (1) A light-sensitive material was applied onto the wafer;
- (2) The patterns on the photomask were scaled down and transferred to the wafer using a stepper (scale factor: 1/5, 1/4 or 1/2);

(3) The wafer provided thereon with the exposed light-sensitive material was developed to form a resist pattern on the wafer;

(4) The wafer was subjected to dry-etching or ion-implantation through the resist pattern.

5       The semiconductor circuits thus produced were a memory circuit (Fig. 8) whose patterns were regularly arranged, a logic circuit whose patterns were randomly arranged and a system LSI circuit (see Fig. 9) comprising combined memory and logic circuits.

10      The memory circuit as shown in Fig. 8 comprises a memory circuit portion whose patterns are regularly arranged and a peripheral circuit portion in which patterns are irregularly arranged in order to ensure the connection to the exterior and these circuit portions differ from each other in the rate of the area occupied by the patterns. In the transistor gate-forming process which has a serious influence on the characteristics of the circuit, the rate of 15     the area removed for patterning on the peripheral circuit portion increases in proportion to an increase in the size of the memory cell portion, i.e. is higher than that of the memory cell portion. In the case of the semiconductor circuit produced according to the present invention, the dimensional difference observed between the memory cell and peripheral circuit portions is very 20     small and on the order of about  $0.004 \mu m$  and this indicates that the amount of the dimensional variation is not more than 2%. Consequently, the patterns never adversely affect the characteristic properties of the resulting circuit. Thus, satisfactory semiconductor circuits could be produced according to the 25     present invention, which did not show any difference between the memory cell and peripheral circuit portions in characteristic properties.

Moreover, the semiconductor circuit or logic circuit produced in this Example was found to have excellent quality, since the same effect described above could also be attained by the logic circuit in which patterns were

randomly distributed and the areas removed for patterning were also randomly distributed and the dimensional difference within the chip was found to be very small on the order of not more than  $0.004 \mu\text{m}$ . It was also found that the present invention permitted the production of an excellent 5 semiconductor circuit or a system LSI circuit comprising combined memory and logic circuit portions (Fig. 9), which did not differ from each other in characteristic properties. This is because the memory and logic circuit portions differ from each other in the density of patterns, but the dimensional difference between the memory cell and logic circuit portions is very small 10 on the order of  $0.004 \mu\text{m}$  and therefore, the patterns never adversely affect the characteristic properties of the resulting circuit.

As has been discussed above in detail, the method of the present invention permits the decrease of the dimensional difference due to the coexistence of coarsely and densely patterned portions in a plane and the 15 production of a high precision pattern-etched product by using a mixed gas, which comprises (a) a reactive ion etching gas as a mixture of an oxygen-containing gas and a halogen-containing gas, and (b) a reducing gas containing at least hydrogen, in the dry-etching process as a means for forming fine patterns.

Moreover, the method for preparing a chrome type half-tone phase-shift photomask according to the present invention permits a photomask which has a small dimensional difference due to the coexistence 20 of the coarse and dense patterns in a plane and whose patterns are highly precisely processed. In addition, the photomask has uniform patterns. Further the semiconductor circuit produced using the photomask of the present invention has a very high integration density.

In addition, the use of hydrogen chloride gas instead of hydrogen gas as the added gas component can ensure safer etching procedures.